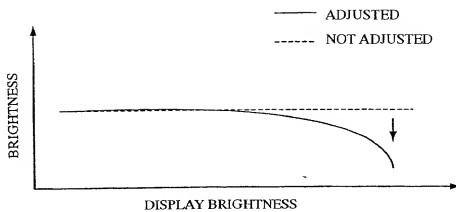


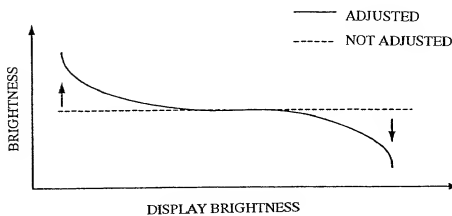


[Figure 2]

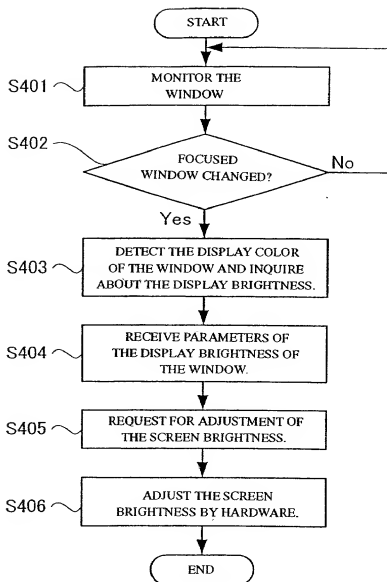
(2/9)



[Figure 3]

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[Figure 5]

(4/9)

**intel®**

### 82371AB PCI-TO-ISA / IDE XCELERATOR (PIIX4)

- Supported Kios for both Pentium™ and Pentium™ Microprocessors
  - 82437TX ISA Kit
  - 82441LX ISA/ADP Kit
- Multifunction PCI to ISA Bridges
  - Supports PCI to ISA Bridges up to 13 MHz
  - Supports PCI Rev 2.1 Specification
  - Supports Full ISA or Extended I/O (EIO) Bus Full Positive Decade or Subtractive Decade of PCI
  - Supports ISA and EIO at 14 of PCI Frequency
- Supports both Mobile and Desktop
  - Deep Core Environment
  - 3.3V Operation with 5V Tolerant Buffers
  - Power for Mobile
  - Environment Support
  - Power-On Suspend, Suspend to RAM, Suspend to Disk, and Soft-Reset
  - All Registers Readable and Writable for Proper Resume from S.V. Suspend
- Power Management Logic
  - Supports Power Management Devices
  - Management
  - Suspend and Resume Logic
  - Supports Thermal Alarm
  - Supports Thermal Control
  - Full Support for Advanced Configuration and Power Interface (ACPI) Revision 1.0 Specification and OS Directed Power
- Integrated 16 x 12-bit Buffer for IDE
  - PCI Burst Transfers
  - Supports Step-less "Step-By-Step" Operation for Electrical Isolation
- Enhanced DMA Support
  - Two 82C77 DMA Controllers
  - Supports PCI DMA with 3 PCI/PCI Channels and Distributed DMA
  - Fast Type-F DMA for Reduced PCI Bus Usage
- Interrupt Controller Based on Two 82C09s
  - 15 Interrupt Support
  - Independently Programmable for Edge Level Sensitivity
  - Supports Shared and APIC
  - Serial Interrupt Input
- Timers Based on 82C54
  - System Timer, Refresh Request, Speaker Tone Output
- USB
  - Two USB 1.0 Ports for Serial Transfers at 12 or 1.5 Mbit/sec
  - Supports Legacy Keyboard and Mouse Software with USB-based Support
  - Supports UHCI Design Guide
- SMBus
  - Host Interface Allows CPU to Communicate Via SMBus
  - Supports Remote Wakeup and Control
  - SMBus Master to Control Resume Events
- RealTime Clock
  - 56.8-KHz 32-Bit 82C50B

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[Figure 6]

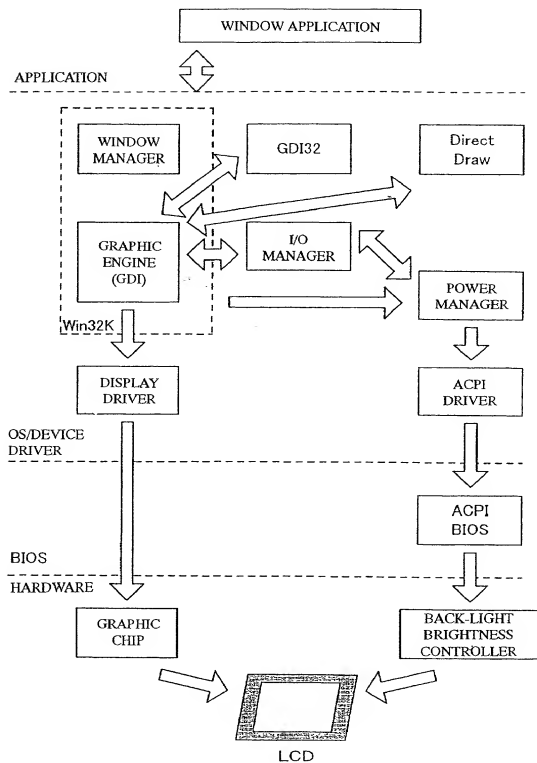
(5/9)



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[Figure 7]

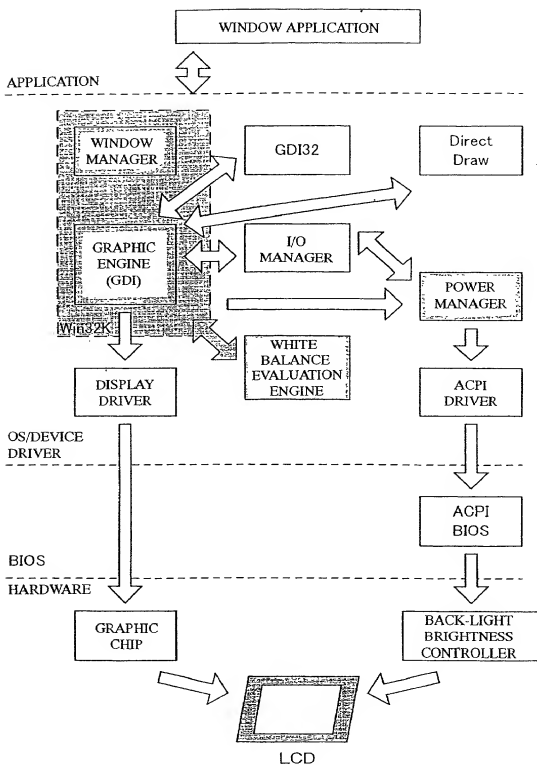
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[Figure 8]

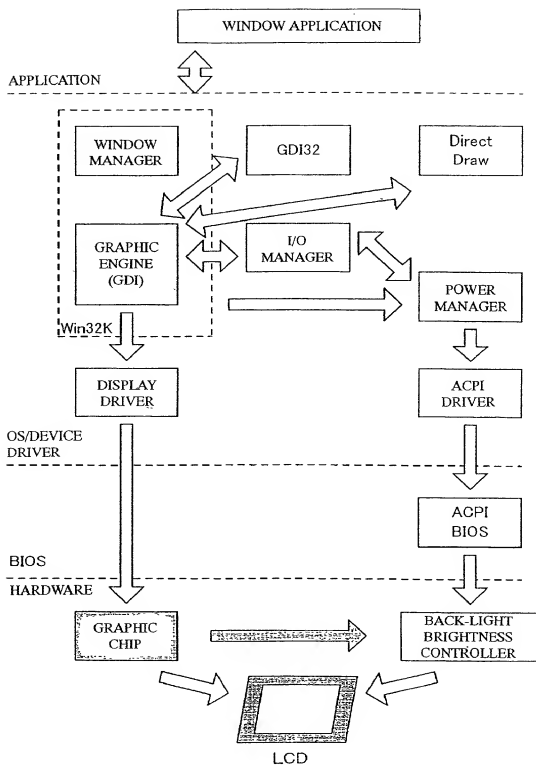
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[Figure 9]

(8/9)



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